



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Boon et al.

Serial No.: 10/717,421

Filed: November 19, 2003

For: CASTELLATED CHIP-SCALE
PACKAGES AND METHODS FOR
FABRICATING THE SAME

Confirmation No.: 7753

Examiner: J. Mitchell

Group Art Unit: 2813

Attorney Docket No.: 2269-5660US
(02-1391.00/US)

Notice of Allowance Mailed:

October 18, 2006

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EV826303924US

Date of Deposit with USPS: January 16, 2007

Person making Deposit: Di Sanders

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

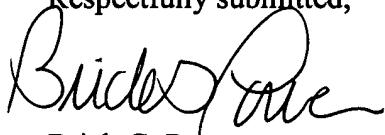
Applicants submit herewith Part B - Fee(s) Transmittal for the above-captioned application and a check in the amount of \$1,700.00 in payment therefor.

Also enclosed are Amendment Pursuant to 37 C.F.R. § 1.312(a) with attached Replacement Sheets (7 sheets) and Annotated Sheets Showing Changes (7 sheets); Comments on

Statement of Reasons for Allowance; and Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees.

Applicants understand that no additional fees are required. However, if the Office determines that any comparison fees or other additional fees are required, the Commissioner is authorized to charge any such fees to TraskBritt Deposit Account No. 20-1469. A copy of this Transmittal Letter is enclosed for deposit account charging purposes.

Respectfully submitted,



Brick G. Power
Registration No. 38,581
Attorney for Applicants
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: January 16, 2007

BGP/dlm:eg

Enclosures: Part B - Issue Fee Transmittal

Check No. 23412 in the amount of \$1,700.00

Copy of Transmittal Letter

Amendment Pursuant to 37 C.F.R. § 1.312(a) (21 pages) with attached

Replacement Sheets (7 sheets) and Annotated Sheets Showing Changes
(7 sheets)

Copy of previously-filed Supplemental Information Disclosure Statement (4 pages),

Form PTO/SB/08 (1 page) with attached non-US references (1 document) and
copy of USPTO date-stamped postcard (1 page)

Comments on Statement of Reasons for Allowance (2 pages)

Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages)

THE PATENT & TRADEMARK OFFICE MAILROOM DATE
STAMPED HEREON IS AN ACKNOWLEDGEMENT THAT ON THIS
DATE THE PATENT & TRADEMARK OFFICE RECEIVED:

Transmittal Form (1 page in duplicate); Response to Restriction/Election or
Species Requirement dated June 1, 2005 (3 pages); Supplemental
Information Disclosure Statement (4 pages); PTO/SB/08A (2 pages); Copies
of non-US documents cited (7 documents)

Invention: CASTELLATED CHIP-SCALE PACKAGES AND
METHODS FOR FABRICATING THE SAME

Applicant(s): Boon et al.

Filing Date: November 19, 2003

Serial No.: 10/717,421

Date Sent: July 1, 2005 via first class mail

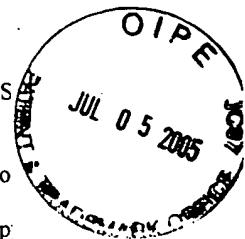
Docket No.: 2269-5660US

BGP/eg

RECEIVED

JUL 11 2005

TRASKBRITT, P.C.





PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Boon et al.

Serial No.: 10/717,421

Filed: November 19, 2003

For: CASTELLATED CHIP-SCALE
PACKAGES AND METHODS FOR
FABRICATING THE SAME

Confirmation No.: 7753

Examiner: J. Mitchell

Group Art Unit: 2813

Attorney Docket No.: 2269-5660US
(02-1391.00/US)

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EV826303924US

Date of Deposit with USPS: January 16, 2007

Person making Deposit: Di Sanders

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

A statement of reasons for allowance accompanied an Office Action of October 6, 2005, in the above-referenced application. That statement provides:

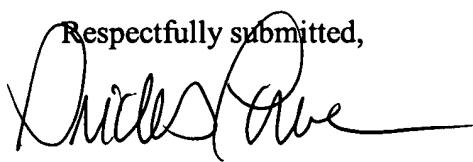
the prior art does not disclose or make obvious forming a redistribution layer over the active surface of the device substrate and in electrical isolation from circuitry of an underlying device including all the limitations of the independent claim.

It is respectfully noted that the above-referenced application includes more than one independent claim, and that the terminology that has been used in the stated reasons for allowance does not appear in independent claim 1. As independent claim 1, which has been

allowed, does not recite "forming a redistribution layer of the active surface of [a] device substrate," the stated reasons for allowance are inapplicable to that claim, as well as to each claim that depends therefrom and lacks the requirement of "forming a redistribution layer."

Further, inasmuch as the stated reasons for allowance are deemed to be applicable to any of the allowed claims, they merely provide examples. The scope of each of the allowed claims should be construed on the basis of the plain meaning of the language used therein, and in accordance with the full, available scope of equivalents to the recited elements.

Respectfully submitted,



Brick G. Power
Registration No. 38,581
Attorney for Applicants
TRASKBRITT, PC
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: January 16, 2007

BGP/dlm:eg
Document in ProLaw